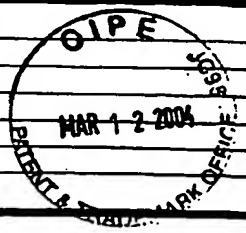



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Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	10/676,264
Date Submitted MAR 12 2004		Filing Date	10/02/2003
(use as many sheets as necessary)		First Named Inventor	Hitoshi TSUNO
Sheet	1 of 1	Group Art Unit	2811
		Examiner Name	Unassigned
		Attorney Docket Number	016907-1577



U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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	B1	K. Ota et al., "Double Offset Implantation Technique for High Performance 80nm CMOSFET with Low Gate Leakage Current", SEMI Forum 2002, ULSI Technology Seminar, Section 4, pages 42-47.		

Examiner Signature	<i>John A. Gal</i>	Date Considered	11/19/04
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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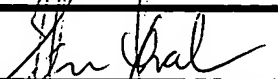
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Substitute for form 1449B/PTO				Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT Date Submitted: October 2, 2003 (use as many sheets as necessary)				Application Number	Unassigned
				Filing Date	10/02/2003
				First Named Inventor	Hitoshi TSUNO
				Group Art Unit	Unassigned
				Examiner Name	Unassigned
				Attorney Docket Number	016907-1577
Sheet	1	of	1		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
PA	A1	6,333,540	B1	SHIOZAWA ET AL.	12-25-2001	
	A2	6,387,743	B1	SHIOZAWA ET AL.	05-14-2002	
	A3	5,994,743		MASUOKA	11-30-1999	

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁵
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
PA	A4	JP	11-220038	A	LSI LOGIC CORP.	08-10-1999		Abstract
	A5	JP	2001-308318	A	mitsubishi electric corp.	11-02-2001		Abstract
	A6	JP	10-223772	A	NIPPON ELECTRIC CO.	08-21-1998		Abstract
	A7	EP	0 932 197	A1	LSI LOGIC CORPORATION	07-28-1999		

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Examiner Signature		Date Considered	11/19/04
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